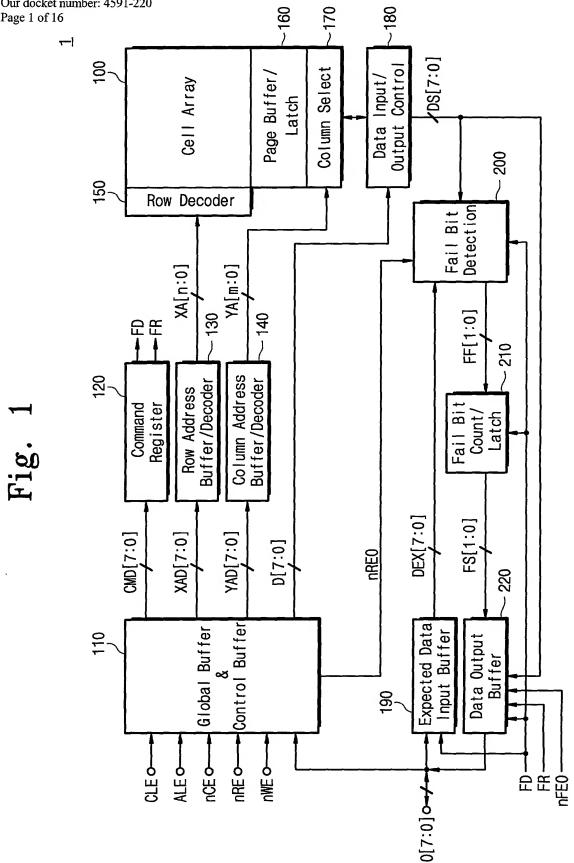
Inventor: .Jae-Yong JEONG and Sung-Soo LEE

Title: NONVOLATILE SEMICONDUCTOR MEMORY
DEVICE WITH A FAIL BIT DETECTING SCHEME AND
METHOD OF COUNTING THE NUMBER OF FAIL
Our docket number: 4591-220
Page 1 of 16

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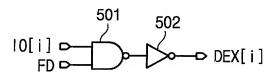
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Page 2 of 16

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Fig. 2

190



Inventor: .Jae-Yong JEONG and Sung-Soo LEE Title: NONVOLATILE SEMICONDUCTOR MEMORY

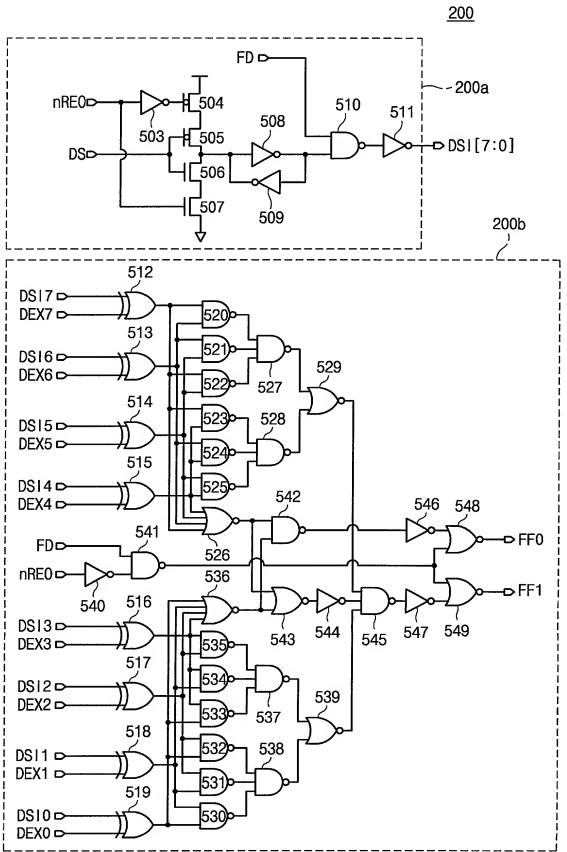
DEVICE WITH A FAIL BIT DETECTING SCHEME AND METHOD OF COUNTING THE NUMBER OF FAIL

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Our docket number: 4591-220 Fig. 3 Page 3 of 16



Our docket number: 4591-220

Page 4 of 16

Fig. 4

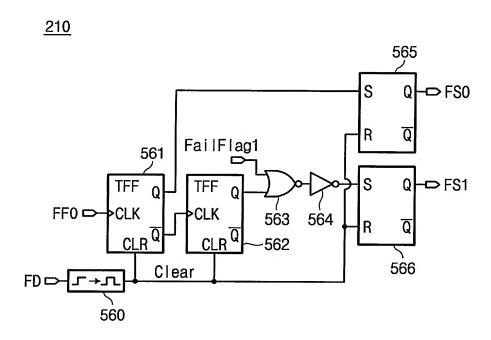
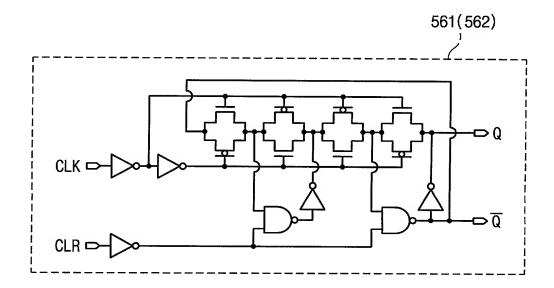


Fig. 5



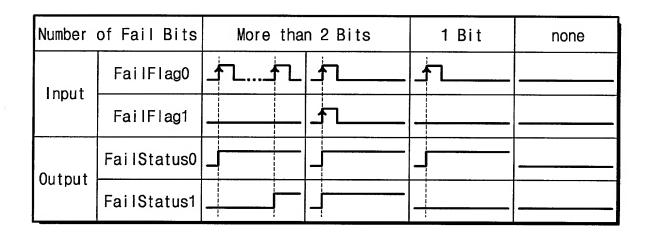
Inventor: .Jae-Yong JEONG and Sung-Soo LEE
Title: NONVOLATILE SEMICONDUCTOR MEMORY
DEVICE WITH A FAIL BIT DETECTING SCHEME AND

METHOD OF COUNTING THE NUMBER OF FAIL

Our docket number: 4591-220

Page 5 of 16

Fig. 6



METHOD OF COUNTING THE NUMBER OF FAIL Our docket number: 4591-220

Page 6 of 16

Fig. 7

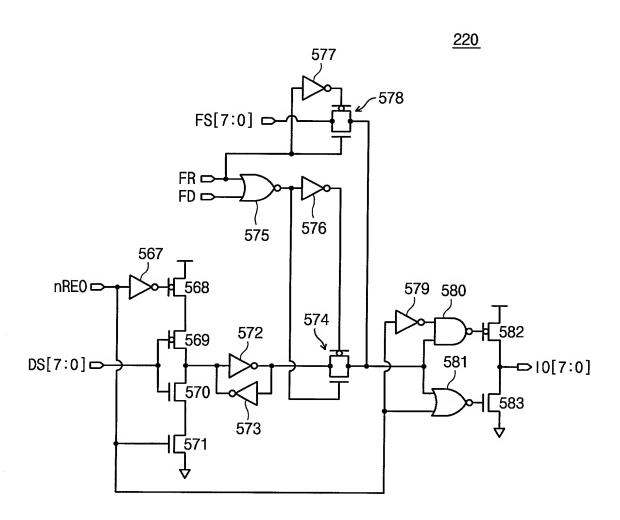
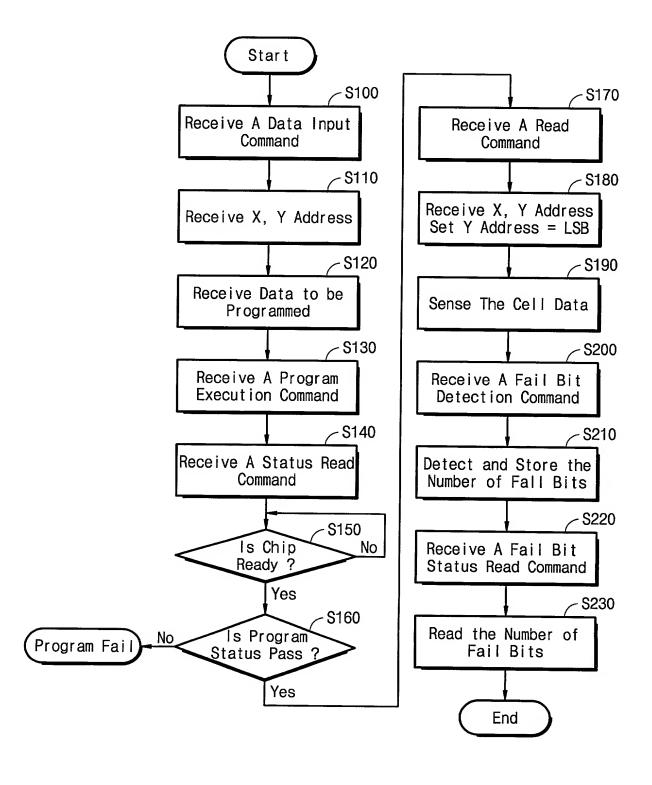


Fig. 8

Our docket number: 4591-220

Page 7 of 16

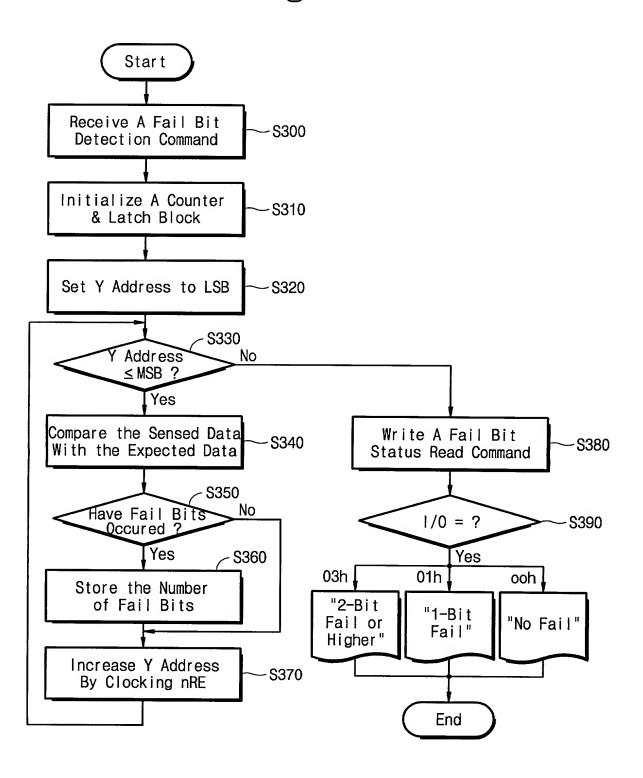
Fig. 9



Our docket number: 4591-220

Page 8 of 16

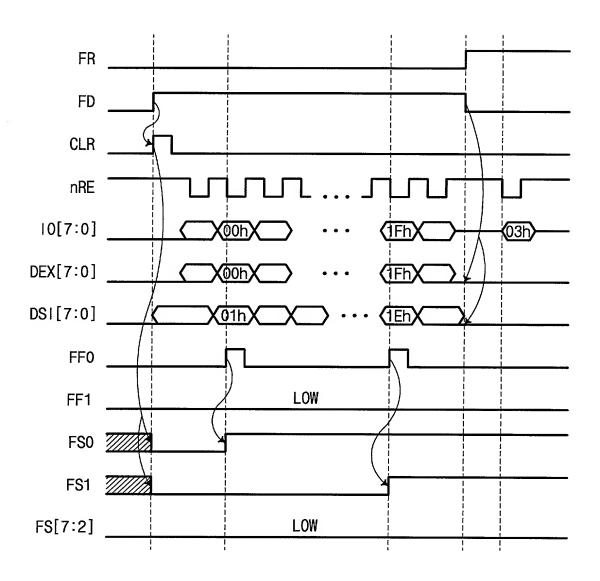
Fig. 10



Our docket number: 4591-220

Page 9 of 16

Fig. 11A

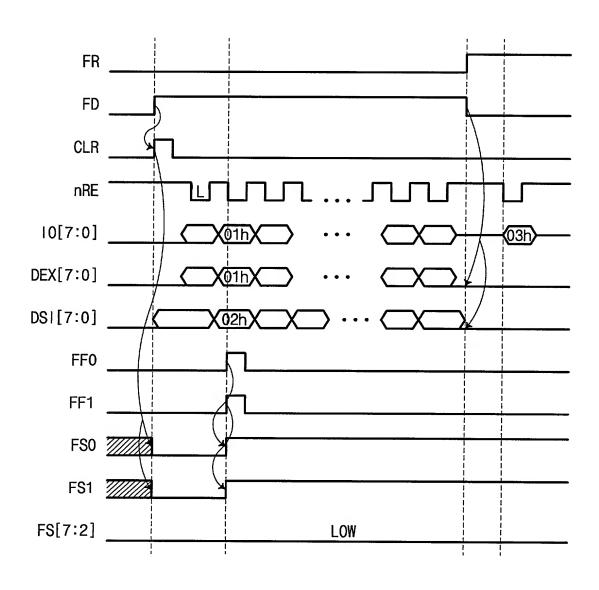


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Our docket number: 4591-220

Page 10 of 16

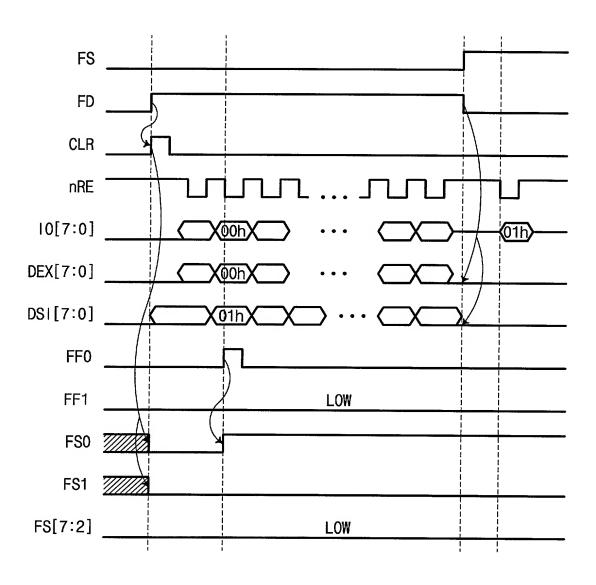
Fig. 11B



METHOD OF COUNTING THE NUMBER OF FAIL Our docket number: 4591-220

Page 11 of 16

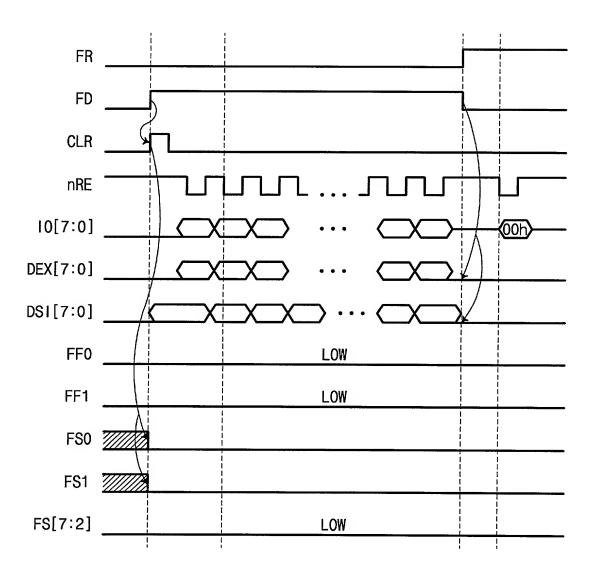
Fig. 12

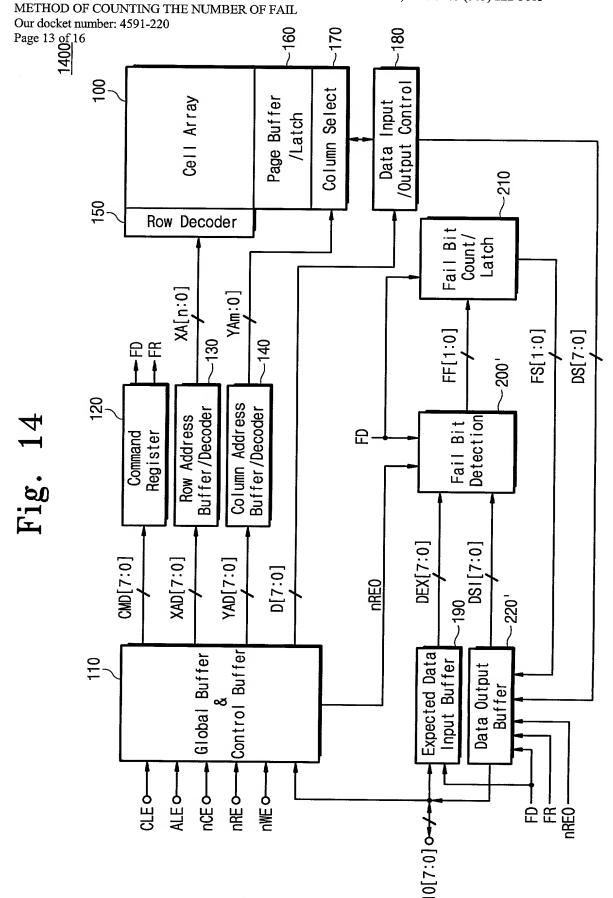


Our docket number: 4591-220

Page 12 of 16

Fig. 13

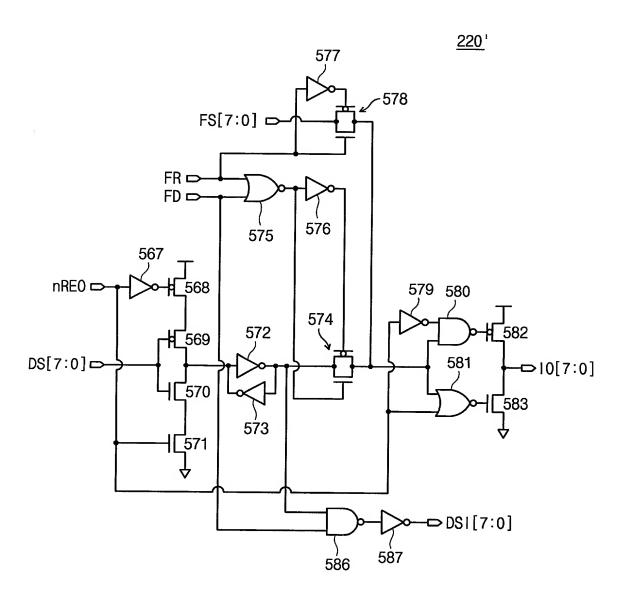




Our docket number: 4591-220

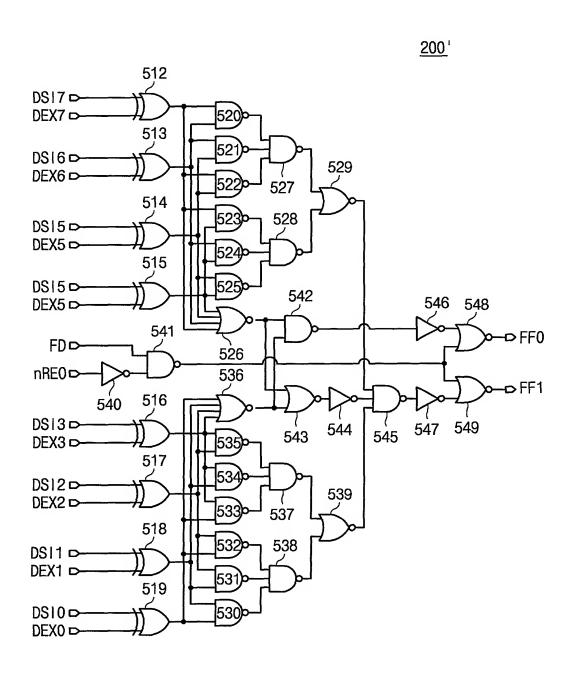
Page 14 of 16

Fig. 15



Page 15 of 16

Fig. 16



METHOD OF COUNTING THE NUMBER OF FAIL

Our docket number: 4591-220

Page 16 of 16

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Fig. 17

Receive Fail Bit Detecting Command Signal -1710 Initialize Counter -1720 Store Test Data In Memory Cells Of The Device -1730 Input Expected Data In An Expected Data 1740 Buffer Of The Device Compare The Stored Expected Data With -1750 The Stored Test Data Register Failure Occasions In Which The Inputted Expected Data Does Not Correspond With -1760 The Compared Outputted Expected Data In A Register Of The Device Count The Registered Failure Occasions -1770 In A Counter Of The Device Output To An Output Data Buffer Of The Device Signals Encoding A Number Of The Counted -1780 Failure Occasions

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